

OpenCPI E3xx RX Test App Guide

Version 1.5

Revision History

Revision	Description of Change	Date
v1.4	Initial release	9/2018
v1.5	Version bump only	4/2019

1 Description

This application is intended to perform a hardware-in-the-loop test of the e3xx_rx.rcc worker. At a minimum, it tests to ensure that the default values and the expected possible min/max values are applied successfully for the various RF frontend parameters.

2 Hardware Portability

This application is intended to test e3xx_rx.rcc, which is by design specific to the e3xx platform.

3 Execution

3.1 Prerequisites

The following must be true before application execution:

- The following assets are built and their build artifacts (FPGA bitstream file/shared object file) are contained within the directory list of the OCPI_LIBRARY_PATH environment variable.
 - for e3xx/xilinx13_4 HDL/RCC platforms:
 - * empty/cnt_1rx_1tx_bypassasm_mode_2_cmos_e3xx assembly/container
 - * e3xx_mimo_xcvr_filter_proxy.rcc
 - * e3xx_rx.rcc
 - * ad9361_config_proxy.rcc (from assets project)
- The current directory is the applications/e3xx_rx.test directory.

3.2 Command(s)

```
./<target-dir>/e3xx_rx_test
```

4 Verification

An application exit status of 0 indicates success, and non-zero indicates failure.